

What is claimed is:

Sub 5 *B7* 1. A computer-implemented method for collecting information relating to execution of an application, the method comprising:

10 determining a set of probe locations in the application at which collecting data relating to the execution of the application would produce non-redundant information; and

15 inserting probes only at the determined probe locations in the application.

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2. The method of claim 1, further comprising determining entry and exit points of a plurality of functions constituting at least a portion of the application.

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3. The method of claim 2, further comprising identifying the entry and exit points as probe locations at which probes are to be inserted.

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4. The method of claim 1, further comprising:

identifying a first location within the application at which a function call directs execution of the application to a second location outside of a current module; and

25 inserting a first probe before the identified first location and a second probe after the identified first location.

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5. The method of claim 4, wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

6. The method of claim 1, further comprising:

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identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and inserting a first probe before the first location and a second probe after the second location.

7. The method of claim 6, wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

8. The method of claim 1, further comprising:
identifying a block of code to which execution of the application is directed upon occurrence of an error; and
15 inserting a first probe at a beginning of the identified block of code and a second probe at an end of the identified block of code.

9. The method of claim 8, wherein the first probe is configured to collect an address of the block of code, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the block of code, a second stack pointer, and a second time indicator.

10. The method of claim 1, further comprising using the inserted probes to collect the information relating to the execution of the application.

25 11. The method of claim 10, further comprising analyzing the collected information.

30 12. A computer-implemented method for collecting information relating to execution of an application, the method comprising:

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determining entry and exit points of a plurality of functions constituting at least a portion of the application;

determining a set of probe locations in the application at which collecting data relating to the execution of the application would produce non-redundant information, the set of probe locations including at least the entry and exit points of the functions;

5 inserting probes only at the determined probe locations in the application;

using the inserted probes to collect the information relating to the execution of the application; and

analyzing the collected information.

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13. The method of claim 12, further comprising:

identifying a first location within the application at which a function call directs execution of the application to a second location outside of a current module; and

inserting a first probe before the identified first location and a second probe after
15 the identified first location,

wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

14. The method of claim 12, further comprising:

identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and

inserting a first probe before the first location and a second probe after the second location,

wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

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15. The method of claim 12, further comprising:
identifying a block of code to which execution of the application is directed upon
occurrence of an error; and
5 inserting a first probe at a beginning of the identified block of code and a second
probe at an end of the identified block of code,
wherein the first probe is configured to collect an address of the block of code, a
first stack pointer, and a first time indicator, and the second probe is configured to collect
the address of the block of code, a second stack pointer, and a second time indicator.

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16. A computer-readable medium having computer-executable instructions
for:

determining a set of probe locations in the application at which collecting data
relating to the execution of the application would produce non-redundant information;
15 and

inserting probes only at the determined probe locations in the application.

17. The computer-readable medium of claim 16, having further computer-
executable instructions for determining entry and exit points of a plurality of functions
20 constituting at least a portion of the application.

18. The computer-readable medium of claim 17, having further computer-
executable instructions for identifying the entry and exit points as probe locations at
which probes are to be inserted.

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19. The computer-readable medium of claim 16, having further computer-
executable instructions for:

identifying a first location within the application at which a function call directs
execution of the application to a second location outside of a current module; and

30 inserting a first probe before the identified first location and a second probe after
the identified first location.

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20 The computer-readable medium of claim 19, wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

21. The computer-readable medium of claim 16, having further computer-executable instructions for:

10 identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and
15 inserting a first probe before the first location and a second probe after the second location.

15 22. The computer-readable medium of claim 21, wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

20 23. The computer-readable medium of claim 16, having further computer-executable instructions for:

25 identifying a block of code to which execution of the application is directed upon occurrence of an error; and

inserting a first probe at a beginning of the identified block of code and a second probe at an end of the identified block of code.

30 24. The computer-readable medium of claim 23, wherein the first probe is configured to collect an address of the block of code, a first stack pointer, and a first time

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25. The computer-readable medium of claim 16, having further computer-executable instructions for using the inserted probes to collect the information relating to the execution of the application.

26. The computer-readable medium of claim 25, having further computer-executable instructions for analyzing the collected information.

10 *Sub A 27* 27. A computer-readable medium having computer-executable instructions for:

determining entry and exit points of a plurality of functions constituting at least a portion of the application;

15 determining a set of probe locations in the application at which collecting data relating to the execution of the application would produce non-redundant information, the set of probe locations including at least the entry and exit points of the functions;

inserting probes only at the determined probe locations in the application;

20 using the inserted probes to collect the information relating to the execution of the application; and

analyzing the collected information.

Sub B 7 28. The computer-readable medium of claim 27, having further computer-executable instructions for:

25 identifying a first location within the application at which a function call directs execution of the application to a second location outside of a current module; and

inserting a first probe before the identified first location and a second probe after the identified first location,

30 wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second

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probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

29. The computer-readable medium of claim 27, having further computer-executable instructions for:

identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and

10 inserting a first probe before the first location and a second probe after the second location,

wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

15 30. The computer-readable medium of claim 27, having further computer-executable instructions for:

identifying a block of code to which execution of the application is directed upon occurrence of an error; and

20 inserting a first probe at a beginning of the identified block of code and a second probe at an end of the identified block of code,

wherein the first probe is configured to collect an address of the block of code, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the block of code, a second stack pointer, and a second time indicator.

25 *Claim 31*
31. A computer arrangement configured to execute computer-executable instructions for:

determining a set of probe locations in the application at which collecting data relating to the execution of the application would produce non-redundant information;

30 and

inserting probes only at the determined probe locations in the application.

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32. The computer arrangement of claim 31, further configured to execute computer-executable instructions for determining entry and exit points of a plurality of functions constituting at least a portion of the application.

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33. The computer arrangement of claim 32, further configured to execute computer-executable instructions for identifying the entry and exit points as probe locations at which probes are to be inserted.

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34. The computer arrangement of claim 31, further configured to execute computer-executable instructions for:

identifying a first location within the application at which a function call directs execution of the application to a second location outside of a current module; and

15 inserting a first probe before the identified first location and a second probe after the identified first location.

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35. The computer arrangement of claim 34, wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

36. The computer arrangement of claim 31, further configured to execute computer-executable instructions for:

25 identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and

inserting a first probe before the first location and a second probe after the second location.

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37. The computer arrangement of claim 36, wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

38. The computer arrangement of claim 31, further configured to execute computer-executable instructions for:

10 identifying a block of code to which execution of the application is directed upon occurrence of an error; and

inserting a first probe at a beginning of the identified block of code and a second probe at an end of the identified block of code.

15 39. The computer arrangement of claim 38, wherein the first probe is configured to collect an address of the block of code, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the block of code, a second stack pointer, and a second time indicator.

20 40. The computer arrangement of claim 31, further configured to execute computer-executable instructions for using the inserted probes to collect the information relating to the execution of the application.

41. The computer arrangement of claim 40, further configured to execute computer-executable instructions for analyzing the collected information.

25 42. A computer arrangement configured to execute computer-executable instructions for:

determining entry and exit points of a plurality of functions constituting at least a portion of the application;

determining a set of probe locations in the application at which collecting data relating to the execution of the application would produce non-redundant information, the set of probe locations including at least the entry and exit points of the functions;

5 inserting probes only at the determined probe locations in the application;

 using the inserted probes to collect the information relating to the execution of the application; and

 analyzing the collected information.

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identifying a first location within the application at which a function call directs execution of the application to a second location outside of a current module; and

inserting a first probe before the identified first location and a second probe after the identified first location,

15 wherein the first probe is configured to collect an address of a first function in which the identified first location is located, an address of a second function in which the second location is located, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the second function, a second stack pointer, and a second time indicator.

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44. The computer arrangement of claim 42, further configured to execute
computer-executable instructions for:

identifying a first location within a calling function at which execution of the application is directed to a called function having an exit point at which execution of the application is directed to a second location outside of the calling function; and

inserting a first probe before the first location and a second probe after the second location,

wherein the first probe is configured to collect an address of the calling function, an address of the called function, a first stack pointer, and a first time indicator, and the second probe is configured to collect the address of the called function, a second stack pointer, and a second time indicator.

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45. The computer arrangement of claim 42, further configured to execute computer-executable instructions for:

identifying a block of code to which execution of the application is directed upon
5 occurrence of an error; and

inserting a first probe at a beginning of the identified block of code and a second probe at an end of the identified block of code,

wherein the first probe is configured to collect an address of the block of code, a first stack pointer, and a first time indicator, and the second probe is configured to collect
10 the address of the block of code, a second stack pointer, and a second time indicator.

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